28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

FEATURES

Built in MIL-STD-461 EMI filter Output trim from 51% to 124% of nominal

- Operating temperature -55° to +125°C
- · Qualified to MIL-PRF-38534 Class H and K
- Radiation hardness assurance (RHA) to level F 300 kRad(Si)
- · Input voltage range 19 to 56 VDC
- Transient protection 80 V for 100 ms
- · Fully isolated, 5 port isolation
- · Dual magnetic feedback
- · Fixed high frequency switching
- · Remote sense
- · Inhibit function
- · Synchronization input
- · Indefinite short circuit protection
- · Radiation tolerant to
 - 1 x 10¹² protons/cm² neutron or proton fluence
 - 10¹¹ rad(Si)/sec dose rate
 - 3 x 105 rad(Si) total dose
 - SEE LET to 80 MeV cm²/mg
- · Meets MIL-STD-704A transient standards



MODELS						
VDC C	DUTPUT					
SINGLE 3.3 5 8.7 12 15	DUAL ±5 ±12 ±15					
+3.3 +3.3 +5.8 +5.8	PLE & ±12 & ±15 & ±7 a ±12 a ±15					

DESCRIPTION

The SMRT28 Series™ of DC/DC converters offers up to 35 watts of power in a radiation tolerant design. The low profile SMRT28 converters are manufactured in Interpoint's fully certified and qualified MIL-PRF-38534 production facility and packaged in hermetically sealed steel cases. They are ideal for use in programs requiring high reliability, small size, and high levels of radiation hardening assurance.

The SMRT28 converters are switching regulators which use a two-phase, phase shifted flyback design with a nominal switching frequency of 300 kHz. Close regulation is maintained with advanced constant frequency pulse width modulation design techniques. The SMRT28's feed-forward compensation and discontinuous topologies provide high levels (80 dB peak) of input-to-output ripple rejection.

A minimum load of 2% of full load should be used on outputs at TID of 100 kRad(Si) or greater to ensure tight regulation over the full input range. The converter should not be inhibited or operated below 19 Vin above 5x10⁴ rad(Si) total dose.

Two independent feedback loops are used to regulate the dual and triple outputs, one feedback loop regulates the two-phased single output. Each set of outputs is electrically isolated from the other and from the input. This product configuration eliminates cross regulation effects between output sets.

Configurations:

- · Single Output: One two-phase single output
- · Dual Output: Two single-phase single outputs
- Triple Output: One single-phase independent Main output and one single-phase dual output. (± Auxiliary output)



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UNDERVOLTAGE LOCKOUT

The converters have an undervoltage lockout that will allow power conversion at approximately 17 volts on a rising input voltage and a conversion shut-down on a falling voltage at approximately 14.5 volts.

INHIBIT FUNCTION

The SMRT28 Series incorporates an inhibit terminal that can be used to disable internal switching. It is not recommended to tie the Inhibit pin of an SMRT28 directly to the Inhibit pin of another converter as the SMRT28 Inhibit pin can sink current. When pulling multiple inhibit signals low a separate interface is recommended for each SMRT28. The converter is inhibited when the inhibit pin is pulled low (0.4 V). In the inhibit mode the inhibit pin current requirement is less than \sim 2 mA. The converter resumes normal operation when a high (\geq 2.4 V) signal or open circuit is applied to the inhibit pin. The open circuit voltage of the inhibit is 5 to 6 volts. Although open circuit is the preferred method for the enabled state, up to 10 volts may be applied to the inhibit pin.

SYNCHRONIZATION

The Sync Input is isolated which allows Sync Return to be tied to the primary side, secondary side, or float with respect to all inputs and outputs. Input current into this pin is limited by a series 1 k ohm resistance.

OUTPUT VOLTAGE TRIM

The output voltage of each set can be trimmed from 51% to 125% of nominal (varies by model, see trim instructions on page 4). The following are "sets" of voltages in relation to trim:

- Triple output models: Main output is one set, the dual outputs are another set.
- Dual outputs: Each output is a "set."
 One set can be trimmed using the trim pin, the other set can be trimmed (up only) using the positive sense pin.
- · Single: One set

RADIATION TOLERANCE

The SMRT28 DC/DC converters are designed to provide continuous normal operation through radiation levels associated with space missions and in tactical and strategic military environments. The converters will operate normally in radiation environments with up to 1 x 10¹² neutrons per square centimeter neutron fluence, 10¹¹ rad(Si) per second dose rate, and 3x10⁵ rad(Si) total dose.

These levels of radiation tolerance make the SMRT28 converters suitable for electronics in programs where operation in high radiation environments is required.

At the highest dose rate (10¹¹ rad(Si)/sec dose rate) there will be an upset resulting in a dip in the voltage. This is non-destructive to the unit. The converter will operate through the upset.

SCREENING

SMRT28 converters offer the following screening options: Space Prototype (O), Class H, or Class K. Radiation tolerant to Radiation Hardness Assurance (RHA) levels of "-" (O), "P", "R" or "F", per MIL-PRF-38534. Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA". See Screening Tables 1, 2 and 3 for more information.

EMI

The SMRT28 has a built-in EMI input filter which brings the conducted emissions into compliance with both methods of MIL-STD-461, revisions C-CE03 and D-/E-/F-CE102.

SENSE

Tight load regulation is maintained via wide bandwidth magnetic feedback and through the use of remote sense on single output models and one output (Output "A") of the dual units. The sense feature can be used to adjust the output voltage by connecting a resistor between the positive sense line and the positive output.

SENSE ADJUST INSTRUCTIONS FOR OUTPUT A ON DUAL MODELS ADJUST UP ONLY

(See trim tables on page 4 for more options)

$$R_{ADJUST} = 240 \left(\frac{Vout - Vnom}{Vmax - Vout} \right)$$

Vout = desired output voltage Vnom = nominal unadjusted output voltage Vmax = maximum achievable output voltage 5V Vmax = 6.0 V 12V Vmax = 14.68 V 15V Vmax = 18.36V

Note: R_{ADJUST} should be connected between Sense A (pin 8) and Vout A (pin 11).

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OPERATING CONDITIONS AND CHARACTERISTICS

Input Voltage Range

- 19 to 56 VDC continuous
- · 80 V for 100 ms transient

Output Power

· Up to 35 watts depending on model

Lead Soldering Temperature (10 sec per pin)

300°C

Storage Temperature Range (Case)

• -65°C to +150°C

Case Operating Temperature (Tc)

- · -55°C to +125°C full power
- · -55°C to +135°C absolute

Derating Output Power/Current

Linearly from 100% at 125°C to 0% at 135°C

Power Dissipation (Pd)

• 20 to 31 W

Output Voltage Temperature Coefficient

100 ppm/°C max

Current Limit

· 125% of full load typical

Isolation at 25°C

100 megohm minimum at 500 V

Audio Rejection

• 50 dB typical

Conversion Frequency

• 300 kHz typ 270 kHz min, 330 kHz max

Radiation (will operate through)

- 1x10¹² neutrons/cm² neutron or proton fluence
- Radiation dose rate 10¹¹ rad(Si) /sec
- Total dose up to 300 kRad(Si)
- SEE LET to 80 MeV cm²/mg

SYNC AND INHIBIT

Svnc

- · Input frequency 270-330 kHz
- · Duty cycle 20% min, 80% max
- 2.5 V p-p minimum amplitude
- 10 V p-p maximum amplitude
- · Sync circuit is floating and isolated
- If sync is used at lower end of the range, full load operation is not guaranteed.

Inhibit

- Active low (output disabled)
 - ► INH referenced to input common
 - ► Active low, pulled to 0.4 V or lower
 - ► Inhibit pin current ≤ 2mA
- Active high (output enabled)
 - ► High >2.4V
 - ► Not to exceed 10V
 - Open pin voltage 5 to 6 volts

MECHANICAL AND ENVIRONMENTAL

Size (maximum)

- 2.705 x 2.205 x 0.400 inches (68.71 x 56.01 x 10.16 mm)
- ► See case S for dimensions.

Weight (maximum)

· 100 grams maximum

Screening

Space Prototype, Class H, or Class K are radiation tolerant to Radiation Hardness Assurance (RHA) levels of "-" (O), "P", "R", or "F" per MIL-PRF-38534. Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA".

See Screening Tables 1, 2 and 3 for more information. Available configurations: OO, HP, HR, KP, KR, KF

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TRIM TABLES

TRIM DOWN

Singles

 R_{TRIM} should be connected between the **Trim** pin and **Positive Sense** pin. R_{TRIM} should be connected between the **Trim** pin and **Sense Return** pin.

 $R_{\scriptscriptstyle TRIM}$ should be connected between the Trim B pin and Positive Vout B pin. This only trims output B, output A can use the sense pin to trim up only. See page 2 for instructions on trimming up output A.

 \mathbf{R}_{TRIM} should be connected between the $\mathbf{Trim}~\mathbf{A}$ pin and $\mathbf{Positive}~\mathbf{Vout}$ A pin to adjust the Main output.

 \mathbf{R}_{TRIM} should be connected between the $\mathbf{Trim}\;\mathbf{B}$ pin and $\mathbf{Positive}\;\mathbf{Vout}$ **B** pin to adjust the Auxiliary outputs.

$$R_{TRIM} = K \left(\frac{Vout - Vmin}{Vnom - Vout} \right)$$

Vout = Desired Voltage; K/Vmin/Vnom from table below.

V_{NOM}	K	V _{MIN}	% OF V _{NOM}
3.3	3500	3.18	96
5.0	4520	4.16	83
7.0	8020	5.47	78
8.7	9040	6.13	70
12	11030	7.05	58
15	12900	7.65	51

TRIM UP

Singles

 ${\rm R}_{\rm TRIM}$ should be connected between the ${\bf Trim~B}$ pin and ${\bf Vout~B~Return}$ pin. This only trims output B, output A can use the sense pin to trim up only.

 R_{TRIM} should be connected between the **Trim A** pin and **Vout A Return** pin to adjust the Main outputs.

 \mathbf{R}_{TRIM} should be connected between the $\mathbf{Trim}~\mathbf{B}$ pin and $\mathbf{Vout}~\mathbf{B}~\mathbf{Return}$ pin to adjust the Auxiliary outputs.

$$R_{TRIM} = K \left(\frac{Vmax - Vout}{Vout - Vnom} \right)$$

Vout = Desired Voltage; K/Vmax/Vnom from table below.

V_{NOM}	K	V _{MAX}	% OF V _{NOM}
3.3	3000	3.70	112
5.0	.0 3000 6.26		125
7.0	5300	8.27	118
8.7	5300	10.46	149
12	5300	14.69	122
15	5300	18.54	124

Note: Do not exceed maximum current rating when trimming down,

14 13 SMRT Single 10 0 0 14 Vout B 13 SMRT Dual 10 $\overline{\circ}$ 0 0 0 Vout A 13 Trim A 12 SMRT Triple 10 0 0 0

FIGURE 1: TRIM DOWN DIAGRAMS

Note: Do not exceed maximum power rating when trimming up.

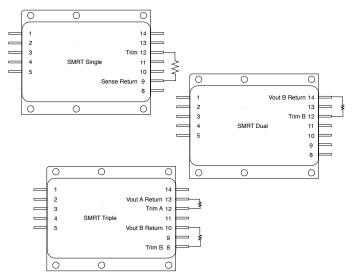


FIGURE 2: TRIM UP DIAGRAMS

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	PIN OUT									
Pin	Pin Single Output Dual Output Triple Output									
1	Vin	Vin	Vin							
2	Vin Return	Vin Return	Vin Return							
3	Inhibit	Inhibit	Inhibit							
4	Sync In	Sync In	Sync In							
5	Sync Return	Sync Return	Sync Return							
8	Sense	Sense A	Trim B							
9	Sense Return	Sense A Return	Vout B Negative							
10	Vout A Return	Vout A Return	Vout B Return							
11	Vout A	Vout A	Vout B Positive							
12	Trim	Trim B	Trim A							
13	Vout A	Vout B	Vout A							
14	Vout A Return	Vout B Return	Vout A Return							

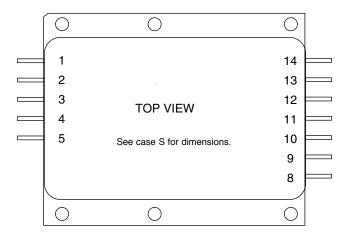


FIGURE 3: PIN OUT

PINS NOT IN USE							
Inhibit: single, dual and triple	Leave unconnected						
Sync In	Connect to Sync Return						
Sense Lines	Must be connected to appropriate outputs						
Trim (all)	Leave unconnected						

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SMD NUMBERS									
STANDARD MICROCIRCUIT DRAWING (SMD)	SMRT28 SIMILAR PART								
5962F0622001KXC	SMRT283R3S/KF								
5962F0622101KXC	SMRT2805S/KF								
5962F0821001KXC	SMRT2812S/KF								
5962F0622201KXC	SMRT2815S/KF								
5962F0622301KXC	SMRT2805D/KF								
5962F0720201KXC	SMRT2812D/KF								
5962F0622401KXC	SMRT2815D/KF								
5962F0821101KXC	SMRT283R312T/KF								
5962F0821201KXC	SMRT283R315T/KF								
5962F0622501KXC	SMRT28512T/KF								
5962F0622601KXC	SMRT28515T/KF								

The SMD number shown is for Class K screening and Radiation Hardness Assurance (RHA) level F. See the SMD for the numbers for other screening and radiation levels. For exact specifications for an SMD product, refer to the SMD drawing. SMDs can be downloaded from: http://www.dscc.dla.mil/programs/smcr

MODEL SELECTION ON THE LINES BELOW, ENTER ONE SELECTION FROM EACH CATEGORY TO DETERMINE THE MODEL NUMBER. SMRT28 **CATEGORY** Base Model and Output Voltage 1 Number of Outputs 2 Screening 3 RHA⁴ Input Voltage 3R3, 05, 12, 15 s 0 0 SMRT28 is the only 05, 12, 15 D **SELECTION** available selection 3R312, 3R315, 512, 515 Т R

Notes

- 1. Output Voltage: An R indicates a decimal point. 3R3 is 3.3 volts out. The 3R3 output voltage is only available in single and triple output models. The 3R312 and 3R315 triple output converters are +3.3 volt main and ±12 or ±15 volt auxiliaries. 512 and 515 triple output converters are +5 volt main and ±12 or ±15 volt auxiliaries.
- 2. Number of Outputs: S is a single output, D is a dual output, and T is a triple output
- 3. Screening: A screening level of O is a Space Prototype and is only used with RHA O. See Screening Tables 2 and 3 for more information.
- 4. RHA: Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA." RHA O is only available with Screening level O. See Screening Table 3 for more information.

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Electrical Characteristics: -55°C to +125°C T_C, 28 VDC Vin, 100% load, unless otherwise specified.

SINGLE OUTPU	T MODELS	SM	IRT283F	R3S	SN	MRT280	5S	SMRT288R7S			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		3.23	3.30	3.37	4.90	5.00	5.10	8.52	8.70	8.87	VDC
OUTPUT CURRENT ²		0	_	6.97	0	_	6.0	0	_	4.0	Α
OUTPUT POWER ²	V _{IN} = 19 TO 56 VDC	0	_	23	0	_	30	0	_	35	W
OUTPUT RIPPLE	10 kHz - 20 MHz	_	_	280	_	_	200	_	_	200	mV p-p
LINE REGULATION ²	V _{IN} = 19, 50 VDC	_	5	30	_	5	30	_	5	30	mV
LOAD REGULATION		_	5	40	_	5	40	_	5	80	mV
	V _{IN} = 19, 50	_	-	90	_	_	70	_	_	80	1111
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	3.0	_	3.6	4.7	_	5.3	8.3	_	9.1	VDC
INPUT VOLTAGE	CONTINUOUS	19	28	56	19	28	56	19	28	56	VDC
	TRANSIENT ¹ 100 ms	0	_	80	0	_	80	0	_	80	V
INPUT CURRENT	INHIBITED	_	-	50	_	_	50	_	_	50	
	NO LOAD	_	_	110	_	_	110	_	_	115	mA
INPUT RIPPLE CURRENT 3	10 kHz - 20 MHz	-	10	50	_	10	50	_	10	50	mA p-p
EFFICIENCY		60	_	-	66	_	_	72	_	_	%
LOAD FAULT 4, 5	POWER DISSIPATION	_	_	31	_	_	28	_	_	25	W
	RECOVERY 1	_	_	25	_	_	25	_	_	25	ms
STEP LOAD RESPONSE 5, 6, 7	TRANSIENT	-	_	±450	_	_	±450	_	_	±450	mV pk
	RECOVERY	_	_	3	_	_	3	_	_	4	ms
STEP LINE RESPONSE 1, 5, 6	19 - 50 - 19 V _{IN} TRANSIENT	_	-	±500	_	-	±500	_	-	±500	mV pk
	RECOVERY	_	_	4	_	_	4	_	_	4	ms
START-UP ⁸	DELAY NO LOAD AND FULL	_	_	35	_	_	25	_	_	60	ms
	OVERSHOOT	_	_	50	_	_	50	_	_	95	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	5000	_	_	5000	μF

Notes

- 1. Guaranteed by design, not tested.
- 2. To maintain tight regulation over the full input voltage range a minimum load of 2% of full load should be used on all outputs.
- 3. Converters meet MIL-STD-461 specification revisions for conducted emissions C-CE03 and D-/E-/F-CE-102. The actual value of input ripple current is much less, the limit in the characteristic table is based on measurement resolution..
- 4. Maximum power dissipation when output is shorted.
- 5. Recovery time is measured from application of the transient to point at which V_{OUT} is within 1% of V_{OUT} at final value.
- 6. Transition time \geq 10 μ s.
- 7. Half load to/from full load.
- 8. Measured from release of inhibit or input voltage step

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Electrical Characteristics: -55°C to +125°C T_C, 28 VDC Vin, 100% load, unless otherwise specified.

SINGLE OUTPU	T MODELS	SN	/IRT281	2S	SN			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		11.76	12.00	12.24	14.70	15.00	15.30	VDC
OUTPUT CURRENT ²		_	_	2.92	_	_	2.33	Α
OUTPUT POWER ²	V _{IN} = 19 TO 56 VDC	0	_	35	0	_	35	W
OUTPUT RIPPLE	10 kHz - 20 MHz	_	_	200	_	_	180	mV p-p
LINE REGULATION ²	V _{IN} = 19, 50 VDC	_	5	30	_	5	30	mV
LOAD REGULATION		_	5	40	_	5	40	mV
	V _{IN} = 19, 50 VDC	_	_	90	_	_	90	111 V
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	10.9	_	13.1	14.0	_	16.0	VDC
INPUT VOLTAGE	CONTINUOUS	19	28	56	19	28	56	VDC
	TRANSIENT ¹ 100 ms	0	_	80	0	_	80	٧
INPUT CURRENT	INHIBITED	_	_	50	_	_	50	mA
	NO LOAD	_	_	110	_		110	
INPUT RIPPLE CURRENT 3	10 kHz - 20 MHz	_	10	50	_	10	50	mA p-p
EFFICIENCY		75	_		75			%
LOAD FAULT 4, 5	POWER DISSIPATION	_	_	20	_	_	20	W
	RECOVERY 1	_		25	_		25	ms
STEP LOAD RESPONSE 5, 6, 7	TRANSIENT	_	_	±575	_	_	±575	mV pk
	RECOVERY	_	_	3	_	_	3	ms
STEP LINE RESPONSE ^{1, 5, 6}	19 - 50 - 19 V _{IN} TRANSIENT	_	_	±700	_	_	±700	mV pk
	RECOVERY	-	_	4	_	_	4	ms
START-UP ⁸	DELAY NO LOAD AND FULL	_	_	25	_	_	25	ms
	OVERSHOOT	_	_	120	_	_	150	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	5000	μF

Notes

1. Guaranteed by design, not tested.

- 2. To maintain tight regulation over the full input voltage range a minimum load of 2% of full load should be used on all outputs.
- Converters meet MIL-STD-461 specification revisions for conducted emissions C-CE03 and D-/E-/F-CE-102. The actual value of input ripple current is much less, the limit in the characteristic table is based on measurement resolution.
- 4. Maximum power dissipation when output is shorted.
- 5. Recovery time is measured from application of the transient to point at which V_{OUT} is within 1% of V_{OUT} at final value.
- 6. Transition time \geq 10 μ s.
- 7. Half load to/from full load.
- 8. Measured from release of inhibit or input voltage step

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28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

Electrical Characteristics: -55°C to +125°C T_C, 28 VDC Vin, 100% load, unless otherwise specified.

DUAL OUTP	UT MODELS	SN	/IRT280	5D	SN	/IRT281	2D	SN	/IRT281	5D	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	Vout A	4.90	5.00	5.10	11.76	12.00	12.24	14.70	15.00	15.30	VDC
	Vout B	4.90	5.00	5.10	11.76	12.00	12.24	14.70	15.00	15.30	VDO
OUTPUT CURRENT 2, 3	Vout A	_	_	3.0	_	_	1.46	_	_	1.17	
	Vout B	_	_	3.0	_	_	1.46	_	_	1.17	Α
OUTPUT POWER 2, 3	Vout A	0	_	15	0	_	17.5	0	_	17.5	W
VIN = 19 TO 56 VDC	Vout B	0	_	15	0	_	17.5	0	_	17.5	**
OUTPUT RIPPLE	10 kHz - 20 MHz	_	_	200	_	_	200	_	_	180	mV p-p
LINE REGULATION ²	V _{IN} = 19, 50 VDC	-	_	30	_	5	25	-	5	30	mV
LOAD REGULATION		ı	5	40	_	5	50	_	5	40	mV
	V _{IN} = 19, 50 VDC	_	_	70	_	_	100	_	_	70	
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	±4.6	_	±5.4	±10.9	_	±13.1	±14.0	_	±16.0	VDC
INPUT VOLTAGE ⁴	CONTINUOUS	19	28	56	19	28	56	19	28	56	VDC
	TRANSIENT 100 ms	0	_	80	0	_	80	0	_	80	V
INPUT CURRENT	INHIBITED 4	_	_	50	_	_	50	_	_	50	mA
	NO LOAD	_	_	110	_	_	110	_	_	110	
INPUT RIPPLE CURRENT ⁴	10 kHz - 20 MHz	_	10	50	_	10	150	_	10	50	mA p-p
EFFICIENCY		66	_	_	75	_	_	75	_	_	%
LOAD FAULT 5, 6	POWER DISSIPATION	-	-	28	_	_	20	_	_	20	W
	RECOVERY 1	_	_	25	_	_	25	_	_	25	ms
STEP LOAD	TRANSIENT	_		±450	_	_	±450	_		±575	mV pk
RESPONSE 6, 7, 8	RECOVERY		_	3	_	_	2	_		3	ms
STEP LINE RESPONSE ^{1, 6, 7}	19 - 50 - 19 V _{IN} TRANSIENT	_	_	±500	_	-	±1000	_	_	±700	mv pk
	RECOVERY	_	_	4	_	_	3	_	_	4	ms
START-UP ⁹	DELAY NO LOAD AND FULL	_	-	25	_	-	20	_	-	25	ms
	OVERSHOOT	_	_	50	_	_	350	_	_	150	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	5000	_	_	5000	μF

Notes

- 1. Guaranteed by design, not tested.
- 2. The specified maximum current/power is available from each output.
- 3. To maintain tight regulation over the full input voltage range a minimum load of 2% of full load should be used on all outputs.

 4. Converters meet MIL-STD-461 specification revisions for conducted emissions C-CE03 and D-/E-/F-CE-102. The actual value of input ripple current is much less, the limit in the characteristic table is based on measurement resolution..
- 5. Maximum power dissipation when output is shorted.
- 6. Recovery time is measured from application of the transient to point at which V_{OUT} is within 1% of V_{OUT} at final value.
- 7. Transition time \geq 10 μ s.
- 8. Half load to/from full load.
- 9. Measured from release of inhibit or input voltage step

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28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

Electrical Characteristics: -55°C to +125°C T_C , 28 VDC Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - S	MRT283R312T	3	.3 (MAII	N)	±12 (A	AUXILIA	RIES)	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	MAIN AND POS. AUX	3.23	3.30	3.37	11.76	12.00	12.24	VDC
	NEG. AUX.	_	_	_	-11.58	-12.00	-12.42	.23
OUTPUT CURRENT ^{2, 3}		_	_	4.5	0	±0.63	1 ¹	Α
	TOTAL	_	_	_	_	_	1.25	A
OUTPUT POWER ^{2, 3}		0	_	15	0	±7.5	12 ¹	W
V _{IN} = 19 TO 56 VDC	TOTAL	_	_	_	_	_	15	**
OUTPUT RIPPLE	10 kHz - 20 MHz	_	_	200	_	_	150	mV p-p
LINE REGULATION	MAIN AND POS. AUX	_	_	30	_	5	30	mV
V _{IN} = 19, 50 VDC	NEG. AUX.	_	_	_	_	10	70	•
LOAD REGULATION	MAIN AND POS. AUX	_	5	40	_	5	40	
	V _{IN} = 19, 50 VDC	_	_	100	_	_	120	
	NEG. AUX. 1	_	_	_	_	10	300	mV
	V _{IN} = 19, 50 VDC ¹	_	_	_	_	_	400	
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	3.0	_	3.6	±10.9	_	±13.1	VDC
CROSS REGULATION $^{1, 4, 5}$ $T_{\rm C} = 25^{\circ}{\rm C}$	EFFECT ON NEGATIVE AUXILIARY	_	_	_	_	_	2.5	%
INPUT VOLTAGE	CONTINUOUS	19	28	56	_	_	_	VDC
	TRANSIENT 100 ms	0	_	80	_	_	_	V
INPUT CURRENT	NO LOAD	_	_	110	_	_	_	mA
	INHIBITED	_	_	50	_	_	_	1117 (
INPUT RIPPLE CURRENT 6	10 kHz - 10 MHz	_	10	50	_	_	_	mA p-p
EFFICIENCY 7		68	_	_	_	_	_	%
LOAD FAULT 8, 9	POWER DISSIPATION	_	_	28	_	_	_	W
	RECOVERY 1	_	_	25	_		25	ms
STEP LOAD RESPONSE 9, 10, 11	TRANSIENT	_		±450	_		±450	mV pk
	RECOVERY	_	_	3	_	_	3	ms
STEP LINE RESPONSE 1, 8, 9	22 - 35 - 22 V _{IN} TRANSIENT	_	_	±500	_	_	±750	mV pk
	RECOVERY	_	_	4	_	_	4	ms
START-UP ¹¹	DELAY NO LOAD AND FULL	_	-	35	_	-	25	ms
	OVERSHOOT	_	0	50	_	0	120	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	1000	μF

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

Electrical Characteristics: -55°C to +125°C T_C , 28 VDC Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - SM	TRIPLE OUTPUT MODEL – SMRT283R315T		.3 (MAII	N)	±15 (A			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	MAIN AND POS. AUX	3.23	3.30	3.37	14.70	15.00	15.30	VDC
	NEG. AUX.				-14.48	-15.00	-15.53	VDO
OUTPUT CURRENT 2, 3		_	_	4.5	0	±0.5	0.8 1	A
	TOTAL	_	-	_	_	_	1.0	A
OUTPUT POWER 2, 3		0	_	15	0	±7.5	12 ¹	W
V _{IN} = 19 TO 56 VDC	TOTAL	_	_	_	_	_	15	**
OUTPUT RIPPLE	10 кНz - 20 MHz	_	_	200	_	_	150	mV p-p
LINE REGULATION	MAIN AND POS. AUX	_	_	30	_	5	30	mV
V _{IN} = 19, 50 VDC	NEG. AUX.				_	10	70	•••
LOAD REGULATION	MAIN AND POS. AUX	_	5	40	_	5	40	
	V _{IN} = 19, 50 VDC	_	-	100	_	-	120	
	NEG. AUX. 1	_	_	_	_	_	300	mV
	V _{IN} = 19, 50 VDC ¹	_	_	_	_	_	400	
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	3.0	_	3.6	±14.0	_	±16.0	VDC
CROSS REGULATION $^{1, 4, 5}$ $T_{C} = 25^{\circ}C$	EFFECT ON NEGATIVE AUXILIARY	_	_	_	_	_	2.5	%
INPUT VOLTAGE	CONTINUOUS	19	28	56	_	_	_	VDC
	TRANSIENT 100 ms	0	_	80	_	_	_	V
INPUT CURRENT	NO LOAD	_	_	110	_	_	_	mA
	INHIBITED	_	_	50	_	_	_	1117 (
INPUT RIPPLE CURRENT 6	10 kHz - 10 MHz	_	10	50	_	_	_	mA p-p
EFFICIENCY 7		68	_	_	_	_	_	%
LOAD FAULT 8, 9	POWER DISSIPATION	_	_	28	_	_	_	W
	RECOVERY 1	_	_	25	_		25	ms
STEP LOAD RESPONSE 9, 10, 11	TRANSIENT	_	_	±450	_	_	±450	mV pk
	RECOVERY	_		3	_		3	ms
STEP LINE RESPONSE 1, 8, 9	22 - 35 - 22 V _{IN} TRANSIENT	_	_	±500	_	_	±750	mV pk
	RECOVERY	_	_	4	_	_	4	ms
START-UP ¹¹	DELAY NO LOAD AND FULL	_	-	35	_	_	25	ms
	OVERSHOOT	-	0	50	_	0	150	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	1000	μF

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

Electrical Characteristics: -55°C to +125°C T_C , 28 VDC Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - S	MRT28507T	:	5 (MAIN	I)	±7 (A	UXILIAI	RIES)	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	MAIN AND POS. AUX	4.90	5.00	5.10	6.86	7.00	7.14	VDC
	NEG. AUX.				-6.79	7.00	-7.21	*20
OUTPUT CURRENT 2, 3		_	_	3.0	0	±1.00	1.2 ¹	Α
	TOTAL	_	_	-	_	_	2.0	A
OUTPUT POWER 2, 3		0	_	15	0	±7.0	11.2 ¹	W
V _{IN} = 19 TO 56 VDC	TOTAL	_	_	-	_	_	14	**
OUTPUT RIPPLE	10 кНz - 20 MHz	_	_	230	_	_	±170	mV p-p
LINE REGULATION	MAIN AND POS. AUX	_	_	70	_	_	100	mV
V _{IN} = 19, 50 VDC	NEG. AUX.	_	_	-	_	-	150	
LOAD REGULATION	MAIN AND POS. AUX	_	5	100	_	5	250	
	V _{IN} = 19, 50 VDC	_	_	150	_	-	250	
	NEG. AUX. ¹	_	_	_	_	_	350	mV
	V _{IN} = 19, 50 VDC ¹	_	_	_	_	_	350	
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	4.7	_	5.3	±6.53	_	±7.47	VDC
CROSS REGULATION ^{1, 4, 5} T _C = 25°C	EFFECT ON NEGATIVE AUXILIARY	_	_	_	_	_	2.5	%
INPUT VOLTAGE	CONTINUOUS	19	28	56	_	_	_	VDC
	TRANSIENT 100 ms	0	_	80	_	_	_	V
INPUT CURRENT	NO LOAD	_	_	110	_	_	_	mA
	INHIBITED	_	_	50	_	_	_	110 (
INPUT RIPPLE CURRENT 6	10 kHz - 10 MHz	_	10	50	_	_	_	mA p-p
EFFICIENCY 7		71	_	_	_	_	_	%
LOAD FAULT 8, 9	POWER DISSIPATION	_	_	22	_	_	_	W
	RECOVERY 1	_	_	25	_	-	25	ms
STEP LOAD RESPONSE 9, 10, 11	TRANSIENT	_	_	±300	_	-	±400	mV pk
	RECOVERY	_	_	2	_	_	2	ms
STEP LINE RESPONSE 1, 8, 9	22 - 35 - 22 V _{IN} TRANSIENT	_	_	±500	_	-	±750	mV pk
	RECOVERY	_	_	4	_	_	4	ms
START-UP ¹¹	DELAY NO LOAD AND FULL	_	_	20	_	_	20	ms
	OVERSHOOT	_	0	100	_	0	150	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	1000	μF

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

Electrical Characteristics: -55°C to +125°C T_C , 28 VDC Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - SI	MRT28512T	;	5 (MAIN	1)	±12 (A	AUXILIA	RIES)	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	MAIN AND POS. AUX	4.90	5.00	5.10	11.76	12.00	12.24	VDC
	NEG. AUX.				-11.58	-12.00	-12.42	VDO
OUTPUT CURRENT ^{2, 3}		_	_	3.0	0	±0.63	1 ¹	A
	TOTAL	_	_	_	_	_	1.25	А
OUTPUT POWER ^{2, 3}		0	_	15	0	±7.5	12 ¹	W
V _{IN} = 19 TO 56 VDC	TOTAL	_	-	_	_	_	15	••
OUTPUT RIPPLE	10 kHz - 20 MHz	_	_	200	_	_	150	mV p-p
LINE REGULATION	MAIN AND POS. AUX	_		30	_	5	30	mV
V _{IN} = 19, 50 VDC	NEG. AUX.				_	20	70	
LOAD REGULATION	MAIN AND POS. AUX	_	5	40	_	5	40	
	V _{IN} = 19, 50 VDC	_	_	100	_	_	120	
	NEG. AUX. ¹	_	_	_	_	10	300	mV
	V _{IN} = 19, 50 VDC ¹	_	_	_	_	_	400	
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	4.7	_	5.3	±10.9	_	±13.1	VDC
CROSS REGULATION ^{1, 4, 5} T _C = 25°C	EFFECT ON NEGATIVE AUXILIARY	_	_	_	_	_	2.5	%
INPUT VOLTAGE	CONTINUOUS	19	28	56	_	_	_	VDC
	TRANSIENT 100 ms	0	_	80	_	_	_	V
INPUT CURRENT	NO LOAD	_	_	110	_	_	_	mA
	INHIBITED	_	_	50	_	_	_	1117 (
INPUT RIPPLE CURRENT 6	10 kHz - 10 MHz	_	10	50	_	_	_	mA p-p
EFFICIENCY 7		68	_	_	_	_	_	%
LOAD FAULT ^{8, 9}	POWER DISSIPATION	_	_	25	_	_	_	W
	RECOVERY 1	_	_	25	_	_	25	ms
STEP LOAD RESPONSE 9, 10, 11	TRANSIENT	_	_	±450	_	_	±450	mV pk
	RECOVERY	_		3	_	_	3	ms
STEP LINE RESPONSE 1, 8, 9	22 - 35 - 22 V _{IN} TRANSIENT	_	_	±500	_	_	±750	mV pk
	RECOVERY	_	_	4	_	_	4	ms
START-UP ¹¹	DELAY NO LOAD AND FULL	_	_	25	_	_	25	ms
	OVERSHOOT	_	0	50	_	0	120	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	1000	μF

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

Electrical Characteristics: -55°C to +125°C T_C , 28 VDC Vin, 100% load, unless otherwise specified.

TRIPLE OUTPUT MODEL - SI	MRT28515T	į	5 (MAIN	l)	±15 (AUXILIA	ARIES)	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	MAIN AND POS. AUX	4.92	5.00	5.08	14.77	15.00	15.23	VDC
	NEG. AUX.				-14.70	-15.00	-15.30	,,,,
OUTPUT CURRENT ^{2, 3}		_	_	3.0	0	±0.5	0.80 ¹	Α
	TOTAL	_	_	_	_	_	1	,,
OUTPUT POWER 2, 3		0	_	15	0	±7.5	11.25 ¹	W
V _{IN} = 19 TO 56 VDC	TOTAL	_	_	_	_	_	15	**
OUTPUT RIPPLE	10 kHz - 20 MHz	_	_	200	_	_	200	mV p-p
LINE REGULATION	MAIN AND POS. AUX	_	5	25	_	_	25	mV
V _{IN} = 19, 50 VDC	NEG. AUX.				_	20	35	111 4
LOAD REGULATION	MAIN AND POS. AUX	_	5	50	_	5	50	
	V _{IN} = 19, 50 VDC	_	_	100	_	_	120	
	NEG. AUX. 1	_	_	_	_	_	500	mV
	V _{IN} = 19, 50 VDC ¹	_	_	_	_	_	600	
TOTAL REGULATION ¹ V _{OUT}	ALL CONDITIONS OF LINE, LOAD, AGING, TEMP AND RADIATION	4.7	_	5.3	±14.0	_	±16.0	VDC
CROSS REGULATION 1, 4, 5 T _C = 25°C	EFFECT ON NEGATIVE AUXILIARY	_	_	-	_	_	2.5	%
INPUT VOLTAGE	CONTINUOUS	19	28	56	_	_	_	VDC
	TRANSIENT 100 ms	0	_	80	_	_	_	V
INPUT CURRENT	NO LOAD	_	_	110	_	_	_	mA
	INHIBITED	_	_	50	_	_	_	1117 (
INPUT RIPPLE CURRENT 6	10 kHz - 10 MHz	_	10	150	_	_	_	mA p-p
EFFICIENCY ⁷		70	_	_	_	_	_	%
LOAD FAULT ^{8, 9}	POWER DISSIPATION	_	_	25	_	_	_	W
	RECOVERY 1	_	_	35	_	_	35	ms
STEP LOAD RESPONSE 9, 10, 11	TRANSIENT	_	_	±250	_	_	±350	mV pk
	RECOVERY	_	_	1	_	_	1	ms
STEP LINE RESPONSE 1, 8, 9	22 - 35 - 22 V _{IN} TRANSIENT	_	_	±500	_	_	±750	mV pk
	RECOVERY	_	_	1.5	_	_	1.5	ms
START-UP ¹¹	DELAY NO LOAD AND FULL	_	_	25	_	_	20	ms
	OVERSHOOT	_	0	200	_	0	350	mV pk
CAPACITIVE LOAD ¹ T _C = 25°C	UNCONDITIONALLY STABLE, START-UP DELAY INCREASED	_	_	5000	_	_	1000	μF

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

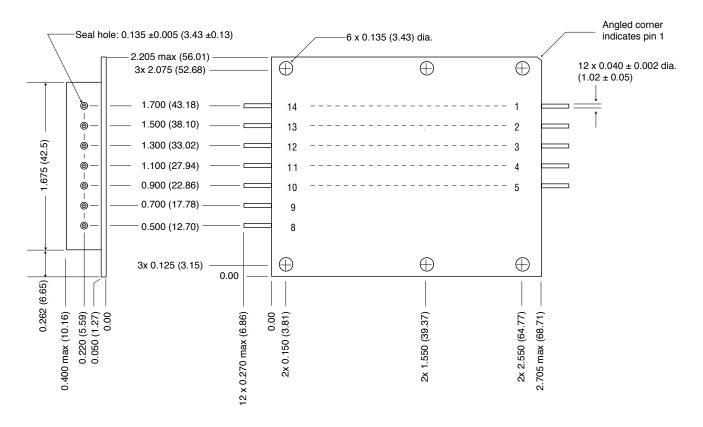
Electrical Characteristics: -55°C to +125°C Tc, 28 VDC Vin, 100% load, unless otherwise specified.

NOTES: TRIPLE OUTPUT MODELS

- 1. Guaranteed by design, not tested.
- 2. Up to the maximum specified auxiliary output current/power is available from either auxiliary output provided the total auxiliary output current/ power does not exceed the total current/power specified .
- 3. To maintain tight regulation over the full input voltage range a minimum load of 2% of full load should be used on all outputs.
- 4. To maintain the cross regulation specification, one of the auxiliaries must always provide a minimum of 20% of the total auxiliary power used. Negative Vout cross regulation is referenced to 50%/50% balanced loads (at 100% of total rated output power - full load).
- 5. Cross regulation is measured under the following conditions:
 - Condition 1: $+P_{OUT} = 20 80\%$,/- $P_{OUT} = 80 20\%$
- Condition 2: P_{OUT} = 20 80%, /+ P_{OUT} = 80 20%
 6. Converters meet MIL-STD-461 specification revisions for conducted emissions C-CE03 and D-/E-/F-CE-102. The actual value of input ripple current is much less, the limit in the characteristic table is based on measurement resolution.
- 7. Efficiency measured with all outputs at full load.
- 8. Maximum power dissipation when all outputs are shorted simultaneously.
- 9. Recovery time is measured from application of the transient to point at which V_{OUT} is within 1% of V_{OUT} at final value.
- 10. Transition time \geq 10 μ s.
- 11. Half load to/from full load.
- 12. Measured from release of inhibit or input voltage step.

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

BOTTOM VIEW CASE S



Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin

Materials

Header Cold Rolled Steel/Nickel

Cover Kovar/Nickel

Pins #52 alloy/gold, ceramic seal

Case S, Rev C, 20091020

Please refer to the numerical dimensions for accuracy. All information is believed to be accurate, but no responsibility is assumed for errors or omissions. Interpoint reserves the right to make changes in products or specifications without notice.

FIGURE 4: CASE S

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

CLASS H AND K, MIL-PRF-38534 ELEMENT EVALUATION

COMPONENT-LEVEL TEST PERFORMED		SPACE PROTOTYPE (O) NON-QML ¹		SS H ML	CLASS K QML		
	M/S ²	P ³	M/S ²	P ³	M/S ²	P ³	
Element Electrical	yes	no	yes	yes	yes	yes	
Element Visual	no	no	yes	yes	yes	yes	
Internal Visual	no	N/A	yes	N/A	yes	N/A	
Temperature Cycling	no	no	no	no	yes	yes	
Constant Acceleration	no	no	no	no	yes	yes	
Interim Electrical	no	N/A	no	N/A	yes	N/A	
Burn-in	no	N/A	no	N/A	yes	N/A	
Post Burn-in Electrical	no	N/A	no	N/A	yes	N/A	
Steady State Life	no	N/A	no	N/A	yes	N/A	
Voltage Conditioning Aging	N/A	no	N/A	no	N/A	yes	
Visual Inspection	no	no	N/A	no	N/A	yes	
Final Electrical	no	no	yes	yes	yes	yes	
Wire Bond Evaluation	no	no	yes	yes	yes	yes	
SEM	no	N/A	no	N/A	yes	N/A	
SLAM™/C-SAM: Input capacitors only (Add'I test, not req. by H or K)	no	no	no	yes	no	yes	

- 1. Non-QML products do not meet all of the requirements of MIL-PRF-38534.
- 2. M/S = Active components (Microcircuit and Semiconductor Die)
- 3. P = Passive components

Element Evaluation: Component testing/screening per MIL-STD-883 as determined by MIL-PRF-38534

SEM: Scanning Electron Microscopy

SLAM™: Scanning Laser Acoustic Microscopy C-SAM: C - Mode Scanning Acoustic Microscopy

SCREENING TABLE 1: ELEMENT EVALUATION

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28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

CLASS H AND K, MIL-PRF-38534 ENVIRONMENTAL SCREENING

END ITEM-LEVEL TEST PERFORMED	SPACE PROTOTYPE (O) NON-QML ¹	CLASS H QML	CLASS K QML
Non-destruct bond pull Method 2023	no	yes ²	yes
Pre-cap Inspection Method 2017, 2032	yes	yes	yes
Temperature Cycle (10 times) Method 1010, Cond. C, -65°C to 150°C, ambient	yes	yes	yes
Constant Acceleration Method 2001, 3000 g	yes	yes	yes
PIND Test Method 2020, Cond. A	no	yes ²	yes
Pre burn-in test	yes	yes	yes
Burn-in Method 1015, 125°C case, typical 96 hours 160 hours 2 x 160 hours (includes mid-BI test)	yes no no	no yes no	no no yes
Final Electrical Test MIL-PRF-38534 Group A, Subgroups 1 through 6 -55°C, +25°C, +125°C case	yes	yes	yes
Radiography Method 2012	N/A	N/A	yes
Post Radiography Electrical Test Room temperature	N/A	N/A	yes ²
Hermeticity Test Fine Leak, Method 1014, Cond. A Gross Leak, Method 1014, Cond. C	yes yes	yes yes	yes yes
Final visual inspection Method 2009	yes	yes	yes

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes

SCREENING TABLE 2: ENVIRONMENTAL SCREENING

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^{1.} Space Prototype (O), non-QML products, do not meet all of the requirements of MIL-PRF-38534.

^{2.} Not required by DSCC but performed to assure product quality.

28 VOLT INPUT - 35 WATT - SPACE QUALIFIED

CLASS H AND K, MIL-PRF-38534 RADIATION HARDNESS ASSURANCE (RHA)

ENVIRONMENTAL SCREENING LEVELS

RADIATION HARDNESS ASSURANCE LEVELS ¹	SPACE PROTOTYPE (O) NON-QML ²	CLASS H QML	CLASS K QML				
O ³ Standard, no radiation guarantee	00	N/A	N/A				
P Radiation tolerant— Tested lots up to 30 kRads (Si) total dose. SEE ⁴ guarantee up to 80 MeV-cm ² /mg	N/A	HP	KP				
R Radiation tolerant— Tested lots up to 100 kRads (Si) total dose. SEE ⁴ guarantee up to 80 MeV-cm ² /mg	N/A	HR	KR				
F Radiation tolerant— Tested lots up to 300 kRads (Si) total dose. SEE ⁴ guarantee up to 80 MeV-cm ² /mg	N/A	N/A	KF				

Notes:



^{1.} Redmond site, Interpoint, has a DSCC approved Radiation Hardness Assurance plan. Our SMD products with RHA "P", "R," or "F" code meet DSCC requirements.

^{2.} Space Prototype (O), non-QML, products do not meet all of the requirements of MIL-PRF-38534.

^{3.} Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) Radiation Hardness Assurance level of MIL-PRF-38534, which is defined as "no RHA"

^{4.} No upset at the pins.